

Figure 1. Throughput for a Slotted ALOHA Channel

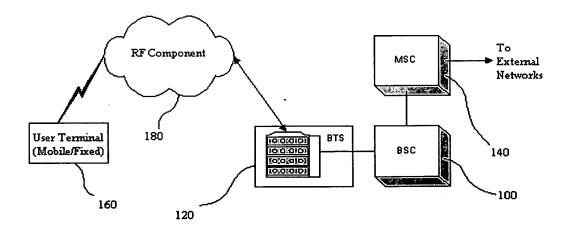


Figure 2. BSC Interfaces

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Figure 3 - Level 1 Congestion Control Transition Matrix

	1	2	4}	7
1 0.2304	72 0.18	698 1 0.0	93491	0
2 0.4804	72 0.28	0472 0.1	40236	0
4 0.7402	36 0.48	0472 0.2	30472	0
7 0.8268	24 0.65	3648 0.4	30472	0

Figure 4 - Comparison of Results

Congestion Control Implemented	Input Rate	Throughput	Mem Delay (ms)	Blocking Probability
NO	0.75	0.182991	2853.925636	0.756001
YES	0.75	0.534169	1013.23703	0.287466

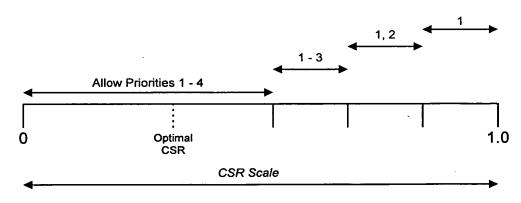


Figure 5. Level 2 Congestion Control

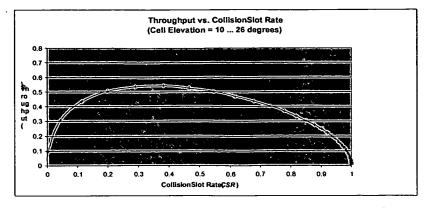


Figure 6 - CSR versus Throughput S

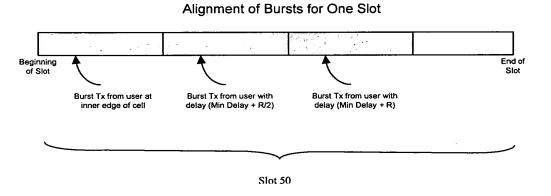
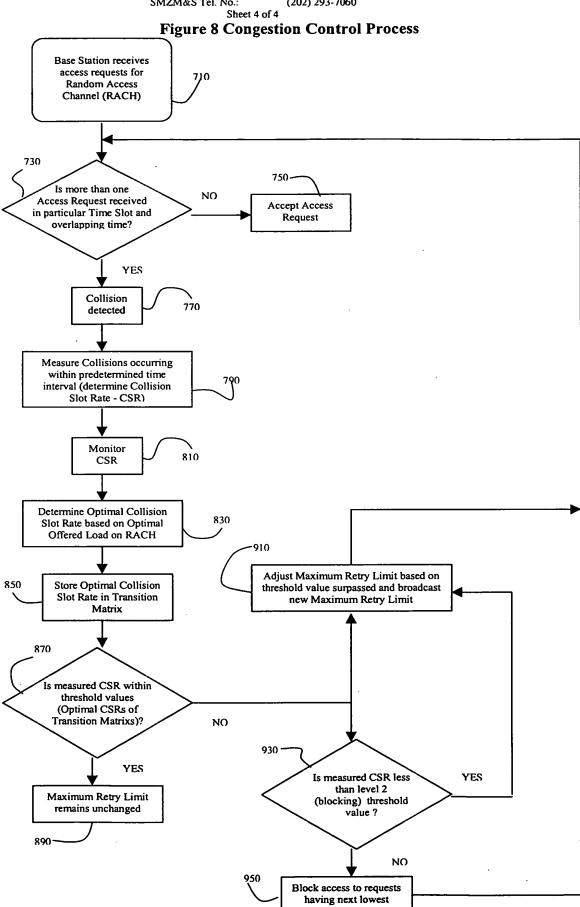


Figure 7 - Burst Alignment within Slotted ALOHA RACH

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priority (1-4)